

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	71	381/111-115.ccls. and microphone SAME (substrate or semiconductor or ((integrated adj3 circuit) or ic))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/26 17:06
S2	55	381/111-115.ccls. and microphone WITH (substrate or semiconductor or ((integrated adj3 circuit) or ic))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/08/15 17:28
S3	8	381/91.ccls. and microphone WITH (substrate or semiconductor or ((integrated adj3 circuit) or ic))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/08/15 17:24
S4	1642	microphone WITH (substrate or semiconductor or ((integrated adj3 circuit) or ic))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/08/15 17:28
S5	1233	microphone WITH (substrate or semiconductor or (integrated adj3 circuit))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/08/15 17:28
S6	807	microphone WITH (substrate or semiconductor)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/08/15 17:29
S7	450	microphone WITH semiconductor	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/08/15 17:30
S8	332	microphone WITH semiconductor	USPAT; EPO; JPO	OR	OFF	2005/08/15 17:45
S9	32	"257"/\$.ccls. and microphone WITH semiconductor	USPAT; EPO; JPO	OR	OFF	2005/10/06 11:54
S10	1	"257"/\$.ccls. and microphone WITH semiconductor SAME packages	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/06 11:55
S11	3	"257"/\$.ccls. and Sheplak-Mark.in.	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/06 12:00
S12	56	381/111-115.ccls. and microphone with (substrate or semiconductor or ((integrated adj3 circuit) or ic))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 15:02
S13	20	381/122.ccls. and microphone with (substrate or semiconductor or ((integrated adj3 circuit) or ic))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 15:04
S14	9	381/91.ccls. and microphone with (substrate or semiconductor or ((integrated adj3 circuit) or ic))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 15:05
S15	21	381/92.ccls. and microphone with (substrate or semiconductor or ((integrated adj3 circuit) or ic))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 15:24
S16	1	257/528.ccls. and microphone with (substrate or semiconductor or ((integrated adj3 circuit) or ic))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 15:06

S17	1	"257"/\$.ccls. and microphone adj3 packages	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 15:07
S18	468	"257"/\$.ccls. and microphone	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 15:07
S19	24	"257"/\$.ccls. and microphone SAME circuit adj3 board	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 15:15
S20	430	"257"/\$.ccls. and sockets SAME circuit adj3 board	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 15:46
S21	3128	"257"/\$.ccls. and (sockets or cavity or opening) SAME circuit adj3 board	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 15:17
S22	3	"257"/\$.ccls. and (sockets or cavity or opening) WITH circuit adj3 board SAME transducer	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 15:19
S23	1	"257"/\$.ccls. and (sockets or cavity) WITH circuit adj3 board SAME microphones	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/10/11 15:23
S24	0	"257"/\$.ccls. and (cavity) WITH circuit adj3 board SAME microphones	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/10/11 15:23
S25	1	381/173.ccls. and (cavity) WITH circuit adj3 board SAME microphones	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/10/11 15:24
S26	23	381/173.ccls. and microphone with (substrate or semiconductor or ((integrated adj3 circuit) or ic))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 15:33
S27	11	381/190.ccls. and microphone with (substrate or semiconductor or ((integrated adj3 circuit) or ic))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 16:41
S28	0	381/190.ccls. and microphone adj3 packages	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 15:36
S29	3	"381"/\$.ccls. and microphone adj3 packages	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 15:37
S30	1	"257"/\$.ccls. and microphone adj3 packages	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 15:37
S31	3	381/92.ccls. and microphone adj3 array SAME (substrate or semiconductor or ((integrated adj3 circuit) or ic))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 16:42

S32	3	381/92.ccls. and microphone adj3 array SAME (substrate or semiconductor or (integrated adj3 circuit))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 16:42
S33	2	381/111-115.ccls. and microphone adj3 array SAME (substrate or semiconductor or (integrated adj3 circuit))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 16:43
S34	0	"257"/\$.ccls. and microphone adj3 array SAME (substrate or semiconductor or (integrated adj3 circuit))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 16:43
S35	1	"257"/\$.ccls. and microphone adj3 array	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 16:44
S36	16995	MEMS	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 16:44
S37	55	"381"/\$.ccls. and MEMS	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 16:56
S38	32	"381"/\$.ccls. and MEMS SAME (substrate or semiconductor or (integrated adj3 circuit))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 16:57
S39	15	381/111-115.ccls. and microphone SAME DC SAME AC	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 17:22
S40	0	381/111-115.ccls. and microphone SAME Wheatstone	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 17:22
S41	2	"381"/\$.ccls. and microphone SAME Wheatstone	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/10/11 17:23